



Part Number

- CM1414ff

Features

- Supply voltage: 5.0V
- Assertion threshold: (1.6V – 1.8V)
- Hysteresis: 100mV
- Current consumption: <math>< 1.5\mu\text{A}</math>
- Temperature range: $-40^{\circ}\text{C} - 125^{\circ}\text{C}$

Applications

- Battery powered equipment
- USB powered equipment
- Battery charger

Technology

- SiTerra 0.18 μm CL180GH5 5V CMOS

Deliverables

- Datasheet/Integration Guide
- HDL Model
- Flat GDSII database/LVS netlist
- Customer Support

PRODUCT OUTLINE

CM1414ff – Power-On Reset

Threshold (1.6V–1.8V), Low Current (1.5 μA)

Status

- Silicon Proven

Overview

This macro-cell is a low consumption Power-On Reset (POR) core designed for SiTerra 0.18 μm CL180GH5 5V CMOS technology.

The threshold sensing voltage ranges from 1.6V to 1.8V (default is 1.65V). A hysteresis of 100mV is added to avoid false reset glitches in noisy supplies.

The POR requires an 1.2V temperature compensated bandgap voltage and 240nA current reference, which can be implemented using **chipus CM1215ff** IP.

The core is easily retargeted to any other 5V CMOS technology.

Functional Diagram

