PRODUCT OUTLINE

CM1413ff – Power-On Reset
Flexible Threshold (1–1.3V), Ultra Low Current (100nA)

Part Number

- CM1413ff

Status

- Silicon Proven

Features

- Supply voltage: 1.0–2.0V
- Configurable assertion threshold (1.0V – 1.3V)
- Adjustable hysteresis (70mV – 200mV)
- Ultra low current consumption (100nA)
- Indicative area: 0.0064mm^2

Overview

This macro-cell is an ultra low consumption Power-On Reset (POR) core designed for SilTerra 0.18μm CL180G CMOS technology.

The threshold sensing voltage can be configured from 1V to 1.3V (default is 1.15V). A hysteresis of 120mV is added to avoid false reset glitches in noisy supplies. This value can be digitally configured from 70mV to 200mV. The POR features an internal process compensated voltage reference. It requires an external 7.75nA current bias (sink), which can be implemented using **chipus CM1014ff** IP.

The core is easily retargeted to any other CMOS technology.

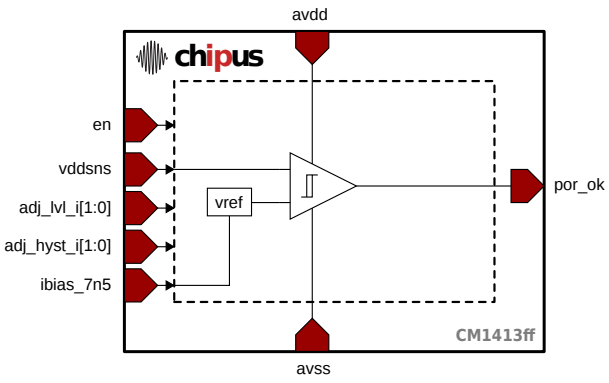
Applications

- Passive/active RFID tag ICs
- Battery powered equipment
- Hearing aids

Technology

- SilTerra 0.18μm CL180G CMOS

Functional Diagram



Deliverables

- Datasheet/Integration Guide
- HDL Model
- Flat GDSII database/LVS netlist
- Customer Support

R. Emilio Blum, 131 – Torre B – sala 308
Centro – Florianopolis – SC – 88020-010 – BRAZIL
Tel. +55 48 3365 2740 – www.chipus-ip.com
Contact ip@chipus-ip.com for further information.