



PRODUCT OUTLINE

CM1312ff – Ultra Low Power PMU

Ultra-low power (2.18 μ W)

Part Number

- CM1312ff

Features

- Shunt regulator (1.8V)
- Current reference (15.5nA)
- Bandgap voltage (1.17V)
- Built-in oscillator (2MHz, 600nA@1.2V)
- Built-in POR
- Indicative area: 0.029mm²

Applications

- Battery powered equipment
- Passive/active RFID tag ICs
- Energy Harvesting ICs
- Hearing aids

Technology

- Silterra 0.18 μ m C18G CMOS

Deliverables

- Datasheet/Integration Guide
- HDL Model
- Flat GDSII database/LVS netlist
- Customer Support

Status

- Silicon Proven

Overview

This macro-cell is an ultra low consumption Power Management Unit (PMU) core designed for Silterra 0.18 μ m C18G CMOS technology TO (thick oxide) and HP (thin oxide) process.

The circuit has a shunt regulator, internal voltage and bias current generators, built-in power-on-reset and oscillator.

The core is easily retargeted to any other CMOS technology with R-poly devices.

Functional Diagram

