



PRODUCT OUTLINE

CM3011bg – General Purpose 10-bit DAC
 10 bits, 2MSPS, buffered output option

Part Number

■ CM3011bg

Features

- General purpose resistive DAC
- 10-bit resolution
- 2MSPS speed
- $avdd = 3.3V$, $dvdd = 1.8V$
- $INL = \pm 0.5 \text{ LSB}$, $DNL = \pm 0.5 \text{ LSB}$
- Buffered output option
- Indicative area: 0.1 mm^2

Applications

- Control applications
- Fan speed control/driver
- General purpose control/driver
- Tuning machines

Technology

- LFoundry $0.15 \mu\text{m}$ LF150 CMOS

Deliverables

- Datasheet/Integration Guide
- HDL Model
- Flat GDSII database/LVS netlist
- Customer Support

Status

- Silicon Under Tests

Overview

This macro-cell is a general purpose, 10-bit, 2MSPS, resistive Digital-to-Analog Converter (DAC) core designed for LFoundry $0.15 \mu\text{m}$ LF150 CMOS technology STD (Standard) and LP (Low Power) process.

The circuit has internal optional buffer to drive directly light loads and is optimized for low power consumption.

The core is easily retargeted to any other CMOS technology with R-poly devices.

Functional Diagram

