



PRODUCT OUTLINE

CM1313ff – General Purpose PMU

Thermal shutdown and overvoltage protected

Part Number

- CM1313ff

Features

- Regulated 1.8V output voltage with overvoltage protection
- Regulated 2.7V output voltage
- Bandgap reference = 1.2V
- Built-in current reference and POR
- Thermal shutdown

Applications

- Battery charger

Technology

- Silterra 0.18 μ m CL18H5 CMOS

Deliverables

- Datasheet/Integration Guide
- HDL Model
- Flat GDSII database/LVS netlist
- Customer Support

Status

- Silicon Proven

Overview

This macro-cell is a Power Management Unit (PMU) core designed for Silterra 0.18 μ m C18GH5 CMOS technology in TO (thick oxide) and HP (thin oxide) process.

The circuit has two linear voltage regulators (one of which has an external feedback connection), internal voltage and bias current generators, built-in power-on-reset (POR), thermal shutdown and overvoltage protection. An internal voltage reference provides a range of voltages for the sake of flexibility of the IP.

The core is easily retargeted to any other CMOS technology with R-poly devices.

Functional Diagram

